

## Contents

Vorwort	v
Preface	vii
List of Tables	xv
List of Figures	xvii
Chapter 1. Introduction	1
1.1. System Analysis	1
1.2. Analysis Methods	2
1.3. Complexity Considerations	3
1.4. Model Order Reduction	4
1.5. Motivations for Symbolic and Numerical Methods	5
1.6. Aim of this Thesis	8
Chapter 2. Foundations	11
2.1. Network Analysis	11
2.1.1. Connection Graphs and Incidence Matrices	12
2.1.2. The Kirchhoff Laws	15
2.1.3. Current-Voltage Relations of Circuit Components	17
2.1.4. Sparse Tableau Analysis	19
2.1.5. Modified Nodal Analysis	20
2.1.6. Further Analysis Methods	22
2.2. Systems of Equations	23
2.2.1. Systems of DAEs	23
2.2.2. Index Concepts	27
2.2.3. Systems of PDAEs	28
2.2.4. Examples	30
2.3. Numerical Analysis Methods	36
2.3.1. Transient Analysis	37
2.3.2. DC Analysis	37
2.3.3. AC Analysis	38
2.4. Systems and Control Theory	41
2.4.1. Reachability	42
2.4.2. Observability	43
2.4.3. Passivity	45
2.4.4. Stability	48
2.5. Model Order Reduction	51

2.5.1. Numerical MOR	51
2.5.2. Symbolic MOR	68
Chapter 3. Hierarchical Systems	77
3.1. Component-based Systems	78
3.2. Coupled and Interconnected Systems	79
3.3. The Behavioral Approach	81
3.4. The Macromodel Concept	84
Chapter 4. Hierarchical Model Order Reduction	87
4.1. Motivating Example	88
4.1.1. Symbolic Reduction of the Entire Circuit	88
4.1.2. Exploitation of the Hierarchical Structure	89
4.1.3. Application of Different Inputs	91
4.1.4. Comparison of the two Approaches	93
4.2. A Workflow for Subcircuit Reductions	93
4.3. Subsystem Sensitivities	95
4.3.1. Term Ranking	96
4.3.2. Clustering	97
4.3.3. Subsystem Sensitivity Analysis	98
4.4. Optimized Order of Subsystem Reductions	100
4.5. Reduction Algorithm Exploiting Circuit Hierarchy	102
4.6. Error Functions	102
4.7. Analysis of the Operational Amplifier op741	106
4.7.1. Reduction using the $\mathcal{L}^2$ -Norm Error Function	108
4.7.2. Reduction using the New Error Function	112
4.7.3. Improvement	116
4.7.4. A Hybrid Reduction Approach	121
4.7.5. Model Quality Check	123
4.7.6. Conclusion	125
Chapter 5. Implementations	129
5.1. GetStateSpace and GetDAE	129
5.2. Arnoldi Iteration	130
5.3. Models for Transmission Lines and LTI State Space Systems	131
5.4. Subcircuit Reductions	134
5.5. Development Environments and Error Functions	138
Chapter 6. Summary and Outlook	139
6.1. Summary	139
6.2. Outlook	142
Appendix A. Network Theory	145
Appendix B. Semiconductor Device Modelling – The Drift-Diffusion Model for the Diode	147
B.1. Physics of a Diode	147

B.2. Current Density and Continuity Equations	149
B.3. The Poisson Equation	151
B.4. Initial and Boundary Conditions	151
B.5. Coupling Conditions	153
Appendix C. The Operational Amplifier op741	155
Bibliography	159
Index	167
Curriculum Vitae	171
Lebenslauf	173