

# Contents

0.1	Abstract	1
0.2	Kurzfassung	3
1.	Introduction	5
1.1	Motivation	5
1.2	Finite Difference Frequency Domain (FDFD) Method	9
1.3	Anisotropic PML Absorbing Boundary Condition	10
1.4	Internal Ports	11
2.	PML Parameters and Convergence	13
2.1	Motivation and General Considerations	13
2.2	Overlapping PML Walls	18
2.3	PML Cell Size	23
	2.3.1 Single PML Wall: Micro-strip Structure	24
	2.3.2 Multiple PML Walls: Patch Antenna	29
	2.3.3 Slot Antenna	34
2.4	PML in Layered Media	35
2.5	Improved Numerical Solver	40
2.6	Conclusion	44
3.	Waveguide Port Simulation with PML	45
3.1	Physical and Artificial Modes	45
3.2	PML Modes and their Coupling to other Modes	51
3.3	Conclusion	55
4.	Internal Port	57
4.1	Relaxation Current Internal Port	58
4.2	Conduction Current Internal port (Line Current)	63
4.3	Parasitic Effects of Conduction Current Internal Ports	66
4.4	Analytical Formulation for the Parasitic Inductance	69
4.5	Simulation with Conduction Current Internal Port	74
	4.5.1 T-junction	74
	4.5.2 HEMT Structure	75
4.6	Conclusion	79

5.	4-Quadrant Slot Antenna	81
5.1	Introduction	81
5.2	Antenna Design	82
5.3	Simulation Results with FDFD	85
5.4	Realization and Measurement Results	91
5.5	Conclusion	95
6.	Summary	96
7.	Appendix	99
7.1	FDFD Discretization	99
7.2	PML Modes and their Coupling to other Modes	100
7.3	Excitation by Relaxation Current (Internal Port) Source	105
7.4	Excitation by Conduction Current (Internal Port) Source	107
7.5	Deembedding Conduction Current Internal Port Parasitics	109
7.6	Excitation with Waveguide Ports	112
7.7	Improvements in the F3D Solver	114
7.8	Acronyms and symbols	116
8.	Literature	119
9	Curriculum Vitae	123

## 0.1 Abstract

For EM simulations of complex MMIC structures, the usage of PML (Perfectly Matched Layer) absorbing boundary conditions is inevitable. PML provides a reflectionless open boundary condition and thus helps to keep the mesh size within a reasonable limit. On the other hand, structures completely enclosed by PML walls do not allow excitation by waveguide ports but require so-called internal ports. Such ports, which are introduced between two or more mesh points inside the structure, facilitate also to include lumped elements or entire networks thus enabling one to study active circuit, for instance.

This work treats both the PML boundary condition and the internal ports, in the framework of the frequency-domain Finite-Difference method (FDFD). It is shown that the presence of PML walls enlarges the magnitude range of the system matrix coefficients, encountered when calculating the fields. This makes the system matrix ill-conditioned and increases the number of iterations when solving the system and may even render this PML totally unusable for most practical applications. It is found that by avoiding any overlapping PML walls and by making the PML cell sizes the largest ones in the whole mesh the high count of the number of iterations can be lowered drastically.

The mode spectrum at waveguide ports with lateral PML walls consists of artificial PML and physical modes. We find that the coupling between PML modes and physical modes must be analyzed thoroughly in order to filter the artificial PML modes out in a reliable way.

Regarding the internal port, a line-current formulation proves to be the only practical choice. The parasitic inductances associated with it are found to be of significant influence. A closed-form expression for these inductances is developed in order to determine their values so that it can be deembedded from the results.

The optimized PML and internal port formulations are verified for the example of a 24 GHz slot antenna with integrated front-end MMIC, suitable for short range communications and sensor networks.